

**LIST OF REFERENCES CITED BY APPLICANT**  
(Use several sheets if necessary)

**ATTY. DOCKET NO.:** 85170-4800  
**APPLICATION SERIAL NO.:** 10/679,978

**APPLICANT:** Stephen Maxwell Parkes  
**FILING DATE:** October 6, 2003  
**GROUP:** ~~2485~~ 2819

**U.S. PATENT DOCUMENTS**

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
DC	AA	4,569,041	2/1986	Takeuchi et al.	370	60	
DC	AB	6,072,944	6/2000	Robinson	395	500.05	
	AC						
	AD						

**FOREIGN PATENT DOCUMENTS**

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
DC	AE	EP 0 338 558	10/1989	Europe	—	—	X	
DC	AF	EP 0 867 820	1/1998	Europe	—	—	X	
DC	AG	EP 0 969 631	1/2000	Europe	—	—	X	
DC	AH	EP 1 096 736	5/2001	Europe	—	—	X	
DC	AI	WO 98/02822	1/1998	PCT	—	—	x	

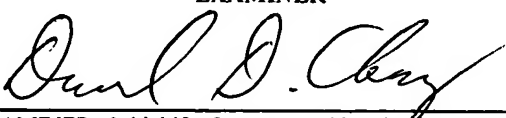
**OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)**

DC	AK	Parkes, "Space Wire: Links, Nodes, Routers and Networks" ECSS-E-50-12 Space Engineering pp. 1-101, 6/2000
DC	AL	Swamy et al. "OO-VHDL, Extensions to VHDL" IEEE (1995)
	AM	<del>Guerrier et al. "A Generic Architecture for On-Chip Packet-Switched Interconnections"</del>
DC	AN	Benini et al. "Networks on Chips: A new SoC Paradigm" SOC Designs IEEE (2002)
DC	AO	A. Tsutsui et al., "Special Purpose FPGA for High-speed Digital Telecommunication Systems", NTT Optical Network Systems Laboratories, pp 486-491, IEEE (1995)
DC	AP	D'Angelo et al., "Modular Design of Communication Node Prototypes" pp. 170-175 IEEE (1997)
DC	AQ	Kaptanoglu et al., (XP-000868474) "A new high density and very low cost reprogrammable FPGA architecture" pp 3-12, (1999)
DC	AR	Vranesic "The FPGA Challenge", Dept. of Electrical and Computer Engineering, pp 121-126 IEEE (1998).

**EXAMINER**  
Paul D. Chang

**DATE CONSIDERED**  
9/23/04

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<b>LIST OF REFERENCES CITED BY APPLICANT</b> <i>(Use several sheets if necessary)</i>					ATTY. DOCKET NO.:		APPLICATION SERIAL NO.:	
					85170-4800		10679978	
					APPLICANT:			
					Stephen Maxwell Parkes			
					FILING DATE:		GROUP:	
<b>U.S. PATENT DOCUMENTS</b>								
*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
DC	AA	4,903,260	2/1990	Boettler et al.	370	60		
↑	AB	5,359,536	10/1994	Agrawal et al.	364	489		
	AC	5,457,410	10/1995	Ting	326	41		
	AD	5,483,178	1/1996	Costello et al.	326	41		
	AE	5,682,107	10/1997	Tavana et al.	326	41		
	AF	5,880,597	3/1999	Lee	326	41		
	AG	5,880,598	3/1999	Duong	326	41		
	AH	5,815,726	9/1998	Cliff	395	800.01		
↓	AI	6,084,429	7/2000	Trimberger	326	41		
DC	AJ	6,191,612 B1	2/2001	Agrawal et al.	326	39		
<b>FOREIGN PATENT DOCUMENTS</b>								
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
DC	AK	0 360 540	3/1990	Europe	—	—	X	
	AL							
<b>OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)</b>								
	AM							
	AN							
<b>EXAMINER</b>					<b>DATE CONSIDERED</b>			
					9/23/0X			
<p><b>*EXAMINER:</b> Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</p>								